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Terms used **debug circuit**

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**1 Advances in functional abstraction from structure**

Richard H. Lathrop, Robert J. Hall, Gavan Duffy, K. Mark Alexander, Robert S. Kirk

June 1988 **Proceedings of the 25th ACM/IEEE conference on Design automation**

Full text available:  pdf(537.20 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

FUNSTRUX has been extended to extract behavioral level models for a commercial simulator directly from a circuit netlist. Recent advances include: a retargetable code generation mechanism; an object-oriented control structure; handling of initialization values; and improved run-time and space requirements of the abstraction process. We discuss some of the issues that arise in translating from LISP to 'C' and from one functional paradigm to another.

**2 An overview of Motorola's PowerPC simulator family**

William Anderson

June 1994 **Communications of the ACM**, Volume 37 Issue 6

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Electronics Manufacturing Technology Symposium, 1991., Eleventh IEEE/CHMT International, 16-18 Sept. 1991

Pages:212 - 214

[\[Abstract\]](#)[\[PDF Full-Text \(172 KB\)\]](#)**IEEE CNF** Print Format
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Circuits and Systems, 2003. ISCAS '03. Proceedings of the 2003 International Symposium on , Volume: 2 , 25-28 May 2003

Pages:II-808 - II-811 vol.2

[\[Abstract\]](#)[\[PDF Full-Text \(404 KB\)\]](#)

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**2 Silicon debug: scan chains alone are not enough***Van Rootselaar, G.J.; Vermeulen, B.;*

Test Conference, 1999. Proceedings. International , 28-30 Sept. 1999

Pages:892 - 902

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





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# Microsoft Developer Studio Generated NMAKE File, Format Version 4.00 # \*\* DO NOT EDIT \*\* # TARGETTYPE " \$(CFG)" == "" CFG=quantum - Win32 **Debug** !MESSAGE No configuration specified.  
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3. **Debug mode without Short-circuit** ⓘ  
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4. [ICD.book](#) (PDF) ⓘ  
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6. <http://www.doc.ic.ac.uk/~ids/dotdot/misc/projects/QuantumComputing/daa98/report-and-source>  
Configuration: QBit - Win32 **Debug** Beginning build with project "H:\Q\_PROJECT\QB Win32 (x86) Application (based ...  
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 ... Dramatically accelerate in-circuit validation of systems with FPGAs ... The Traditional In-Circuit Emulator. Tra  
 equipment available to an ...  
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10. Airline Product Set with MATIP   
 ... config-alps-circ)# alps enable-circuit (config)# alps circuit CKT\_ALC\_2 (config-alps ... is from the **debug** alps  
**circuit** event status change ...  
[akson.sgh.waw.pl/~chopin/ios120/120newft/120t/120t5/alpmatip.htm](http://akson.sgh.waw.pl/~chopin/ios120/120newft/120t/120t5/alpmatip.htm) - 81k - [Cached](#) - [More pages from this site](#)
  
11. LAUTERBACH - TRACE32 ICD In-Circuit Debugger   
 ... The TRACE32 In-Circuit Debuggers are highly ... **Circuit** Emulator TRACE32-ICE using the PODBUS connec  
 can be used to **debug** a ...  
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12. Opto Isolation Circuits for Circuit Debugging of 68HC9(S)12 and 68HC908 MCUs (PDF)   
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 Background **Debug** ModeBKGND 1NC 32 ...  
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 ... debugging features.1.IntroductionAn in-circuit emulator (ICE) is an important tool for the ... modes:i. ForeGrou  
 in-circuit emulator has full ...  
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14. Debug Commands (debug modem - debug ppp)   
 ... Caution Do not enable the **debug** ncia **circuit** command during normal operation because this command gene  
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[akson.sgh.waw.pl/~chopin/ios120/12supdoc/debug\\_r/dmodem.htm](http://akson.sgh.waw.pl/~chopin/ios120/12supdoc/debug_r/dmodem.htm) - 120k - [Cached](#) - [More pages from this site](#)
  
15. The Laboratory Staff will not help debug any circuit whose power supplies have not been prop  
 Last Revision: August 20, 2000Page 1 of 14University of California at BerkeleyPhysics 111 LaboratoryBasic Sem  
 II©2000 by the Regents of the University of California. ... offset triangle wave to **debug** your **circuit**. ( The **circuit**  
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18. iSYSTEM - In-circuit Emulators for Embedded Systems   
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 connects the iC3000 with an ActivePODTM for full in-circuit emulation ... Motorola's Background **Debug** Mode (B  
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19. Introduction   
 ... The **debug** flow is usually influenced by the functionality, hardware **circuit** type, hierarchy and other features o  
**circuit** or it may be ...  
[www.fe.up.pt/~allegro/wp3/d3x\\_wbm/GRA1/debug\\_flow\\_introduction.htm](http://www.fe.up.pt/~allegro/wp3/d3x_wbm/GRA1/debug_flow_introduction.htm) - [More pages from this site](#)

20. [Hitex Development Tools for the Embedded World / debugger/in-circuit emulators/software-too](#)  
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RTOS ... Instead, the in-circuit emulator uses its trace buffer for that purpose ...  
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